

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appln. No:	To Be Assigned
Applicant:	Ricahrd Sita
Filed:	Herewith
Title:	HDTV DOWNCONVERSION SYSTEM
TC/A.U.:	2614
Examiner:	Trang U. Tran
Docket No.:	MATP-39902US1

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and to the duty of disclosure set forth in 37 C.F.R. § 1.56, the Examiner in charge of the above-identified application is requested to consider and make of record the references listed on the PTO 1449 (RP) submitted herewith.

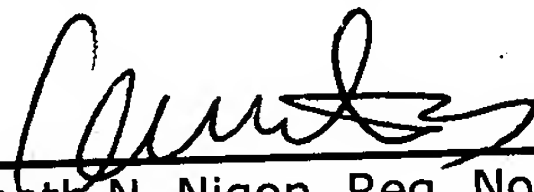
Although the information submitted herewith may be "material" to the Examiner's consideration of the subject application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

Under 37 C.F.R. § 1.98(d), copies of the patents and publications listed on the enclosed PTO Form 1449 are not required to be provided, because they were cited by or submitted to the Patent and Trademark Office in prior application Serial No. 09/180,243, filed April 5, 1999, which is relied upon for an earlier filing date under 35 U.S.C. § 120.

This Information Disclosure Statement is being filed concurrently with the above-referenced application.

Respectfully submitted,


Kenneth N. Nigon, Reg. No. 31,549
Attorney(s) for Applicant(s)

KNN/tmb

Enclosures: PTO Form 1449

Dated: September 26, 2003

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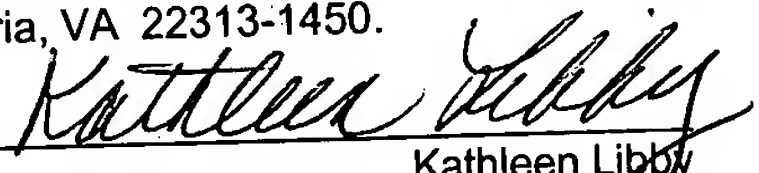
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Kathleen Libby

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Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>	Complete if Known	
	Application Number	To Be Assigned
	Filing Date	Herewith
	First Named Inventor	Richard Sita
	Art Unit	To Be Assigned
	Examiner Name	To Be Assigned
SHEET 1 of 3	Attorney Docket No.	MATP-39902US11

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number - Kind Code ² (if known)				
		US-5,028,995	07/02/1991	Izawa et al.	Entire Document	
		US-4,536,745	08/20/1985	Yamaguchi et al.	Entire Document	
		US-5,519,446	05/21/1996	Lee	Entire Document	
		US-5,737,019	04/07/1998	Kim	Entire Document	
		US-6,292,621 B1	09/08/2001	Tanaka et al.	Entire Document	
		US-5,754,437	05/19/1998	Blazo	Entire Document	
		US-5,262,854	11/16/1993	Ng	Entire Document	
		US-6,262,770 B1	07/17/2001	Boyce et al.	Entire Document	
		US-6,104,753	08/15/2000	Kim et al.	Entire Document	
		US-5,835,151	11/10/1998	Sun et al.	Entire Document	
		US-4,870,661	09/26/1989	Yamada et al.	Entire Document	
		US-5,274,372	12/28/1993	Luthra et al.	Entire Document	
		US-5,481,568	01/02/1996	Yada	Entire Document	
		US-5,613,084	03/18/1997	Hau	Entire Document	
		US-4,774,581	09/27/1988	Shiratsuchi	Entire Document	
		US-5,057,911	10/15/1991	Stec et al.	Entire Document	
		US-5,331,346	07/19/1994	Shields et al.	Entire Document	
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
		EP 0 707 426 A2	04/17/1996	Hitachi, Ltd.	Entire Document	<input type="checkbox"/>
		EP 0 515 143 A2	11/25/1992	Matsushita Electric Industrial Co., Ltd	Entire Document	<input type="checkbox"/>
		JP 08-181988	07/12/1996	Canon, Inc.	Entire Document	<input type="checkbox"/>
		WO 97/01934	01/16/1997	Thomas Multimedia S.A.	Entire Document	<input type="checkbox"/>
		EP 0 598 184 A2	05/25/1994	Kabushiki Kaisha Toshiba	Entire Document	<input type="checkbox"/>
		WO 96/19077	06/20/1996	Philips Electronics N.V.	Entire Document	<input type="checkbox"/>

Examiner Signature		Date Considered	
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¹Applicant's unique citation designation number (optional).

²See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3).

⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

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The collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.**

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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SHEET 2 of 3

Complete if Known

Application Number	To Be Assigned
Filing Date	Herewith
First Named Inventor	Richard Sita
Art Unit	To Be Assigned
Examiner Name	To Be Assigned
Attorney Docket No.	MATP-39902US1

U.S. PATENT DOCUMENTS

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		US-4,468,688	08/28/1994	Gabriel et al.	Entire Document
		US-4,908,874	03/13/1990	Gabriel	Entire Document
		US-4,631,750	12/23/1986	Gabriel et al.	Entire Document
		US-4,472,732	09/18/1984	Bennett et al.	Entire Document
		US-3,997,772	12/14/1976	Crochiere et al.	Entire Document
		US-4,472,785	09/18/1984	Kasuga	Entire Document
		US-5,483,474	01/09/1996	Arbeiter et al.	Entire Document
		US-5,489,903	03/06/1996	Wilson et al.	Entire Document
		US-5,389,923	02/14/1995	Iwata et al.	Entire Document
		US-5,327,235	07/05/1994	Richards	Entire Document
		US-4,652,908	03/24/1987	Fling et al.	Entire Document
		US-5,528,301	06/18/1996	Hau et al.	Entire Document
		US-5,262,854	11/16/1993	Ng	Entire Document
		US-5,737,019	04/07/1998	Kim	Entire Document
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FOREIGN PATENT DOCUMENTS

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		EP 0 725 538 A2	08/07/1996	Sony Corporation	Entire Document	<input type="checkbox"/>
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number	To Be Assigned
Filing Date	Herewith
First Named Inventor	Richard Sita
Art Unit	To Be Assigned
Examiner Name	To Be Assigned
Attorney Docket No.	

SHEET 3 of 3

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		J. BOYCE et al., SDTV Receivers with HDTV Decoding Capability, ACATS Technical Subgroup Meeting, May 18, 1995, Washington, D.C. (18 pgs.)	<input type="checkbox"/>
		K. BLAIR BENSON revised by JERRY WHITAKER, Television Engineering Handbook, Revised Copyright 1992, 1986 by McGraw-Hill, Inc. (7 pgs.)	<input type="checkbox"/>
		J. BOYCE et al., Low-Cost all Format ATV Decoding with Improved Quality, 30 th Advanced Motion Imaging Conference, Paper #11, February 2, 1996 (7 pgs.)	<input type="checkbox"/>
		J. FUHRER, The All Format Decoder, Hitachi America, Ltd., June 3, 1996	<input type="checkbox"/>
		Recommended Practices for Video Encoding Using the ATSC Digital Television Standard-Consideration of Downsampling Decoders, June 3, 1996, (6 pgs.)	<input type="checkbox"/>
		LARRY PEARLSTEIN, Discussion on Recommended Practices for North American ATV Coding, Hitachi America Ltd., Presented to SMPTE WG TVCM, March 8, 1996 (8 pgs.)	<input type="checkbox"/>
		Data Sheet GP9101 High Performance Multirate Digital Filter, Gennum Corporation, January 1994 (21 pgs.)	<input type="checkbox"/>
		A. CUGNINI et al., MPEG-2 Video Decoder for the Digital HDTV Grad Alliance System, 8037 IEEE Transactions on Consumer Electronics, Vol. 41, No. 3, August 1995, pp. 748-753.	<input type="checkbox"/>
		M. M. STOJANCIC et al., Architecture and VLSI Implementation of the MPEG-2:MP@ML Video Decoding Process, 8012 SMPTE Journal, Vol. 104, No. 2, February 1995, pp. 62-72	<input type="checkbox"/>
		International Search Report, June 23, 1998	<input type="checkbox"/>
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